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Instruments CC2530

EM reference design  
with  $T_A = 25^\circ\text{C}$ ,  $V_{DD} = 3\text{ V}$ , and  $f_c = 2440\text{ MHz}$ , unless  
otherwise noted.

**Boldface limits apply  
over the entire  
operating range,  $T_A =$   
 $-40^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_{DD}$   
 $= 2\text{ V}$  to  $3.6\text{ V}$ , and  $f_c$   
 $= 2394\text{ MHz}$  to  $2507\text{ MHz}$ .**

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The  
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contains schematics  
and layout files for the  
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used in the  
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contains two CC2530

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which can be used

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2) In CC2530 datasheet (swru191d section 23.8.12) is written "Note that the CCA signal is updated four clock cycles (system clock) after the `RSSI_VALID` signal has been set.". In TIMAC program code there is no delay between `RFST = WHI`

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```
LE(C_RSSI_IS_INVALID); and RFST =  
SKIP(1,  
C_CCA_IS_VALID);.
```

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processor & CCA -  
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CC2430 Data Sheet  
(rev. 2.1) SWRS036F  
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## ZigBee® Applications

- 2.4 GHz IEEE 802.15.4 systems
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**Description.** Texas Instruments. CC2530. [Old version datasheet] A True System-on-Chip Solution for 2.4-GHz IEEE 802.15.4 and ZigBee Applications. CC2530 F32. [Old version datasheet] A True System-on-Chip Solution for 2.4-GHz IEEE 802.15.4 and ZigBee Applications.

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either as standalone modules (requires external power) or connected to e.g. the SmartRF05EB Evaluation Boards (not included in kit) for full control from the PC. CC2530EMK CC2530 Evaluation Module Kit | TI.com

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Connected to the TI SmartRF Studio 7, the continuous RX-RSSI-graph of the CC2530 sits at roundabout -3dBm constantly, the CCA and LOCK\_STATUS indicators are both off all the time. The CC2531 sits at -105dBm when not receiving packages

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and the indicators are both on. My design is based on the reference designs provided by Texas Instruments.

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